

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein

Title: INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE
UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD
THERFOR

Application No.: 09/990,894

Filed:

November 16, 2001

Examiner: Connie C. Yoha

Group Art Unit:

2818

Atty. Docket No.: 023-0013

August 7, 2003

Mail Stop Non Fee Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE TO NON-FINAL OFFICE ACTION**

This paper is responsive to a Non-Final Office Action mailed May 8, 2003, having a shortened statutory period for response set to expire August 8, 2003. In light of the Amendments and/or Remarks herein, further examination is requested.

Please amend the above-identified application as follows:

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